



Tempus

Development of Embedded System Courses with implementation
of Innovative Virtual approaches for integration of Research,
Education and Production in UA, GE, AM

DESIRE

**TEMPUS-project 544091-TEMPUS-1-2013-1-BE-
TEMPUS-JPCR**

**P11-TSU report on activities
from 1.12.2014 till 1.05.2014**



WP1 Analysis of current curricula and competences in Embedded Systems in TC

- Experts Panel was set up (G. Ghvedashvili, D. Kakulia, T. Gavasheli)
- Curricular analysis
- Students opinion analysis (surveys distribution/analysis)
- Labor Market Analysis (surveys distribution/analysis)
- Analysis of the existing E-learning recourses



WP 4 Retrain academic teachers on ESD engineering and virtual learning platforms

- Two Professors from Department of Electrical and Electronics Engineering and one Ph.D. student from Department of Physics, Faculty of Exact and Natural Sciences, were selected for training
- Departments involved in the project: Department of Electrical and Electronics Engineering, Department of Physics, Department of Computer Science, Department of Scientific R&D



WP 7 Dissemination and Enterprise Collaboration

- Dissemination through web resources
(http://rp.tsu.ge/index.php?option=com_content&view=article&id=352%3Adesire&catid=32%3Anews&Itemid=112&lang=en-GB)
- Dissemination meetings in university (two meetings on dissemination were organized)



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Thank You for Your Attention!